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APPLICANTS
 Hiroyuki Takahashi, Kanagawa, JAPAN;

** CONTINUING DATA ***** *none*

** FOREIGN APPLICATIONS ***** *yes*
 JAPAN 2002-314327 10/29/2002

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|--|---|------------------------------|-------------------------|-----------------------|-----------------------------|
| Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Chih Chung</i> Examiner's Signature Initials | STATE OR COUNTRY JAPAN | SHEETS DRAWING 13 | TOTAL CLAIMS 46 | INDEPENDENT CLAIMS 10 |
|--|---|------------------------------|-------------------------|-----------------------|-----------------------------|

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TITLE
 Semiconductor memory device and control method thereof

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